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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes

Examiner: Dickey, Thomas

Patent No.: 6,963,103

Group Art Unit: 2826

Issue Date: November 8, 2005

Docket No: 1303.028US1

Title: SRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER
INTERPOLY INSULATORS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: CERTIFICATE OF CORRECTION BRANCH

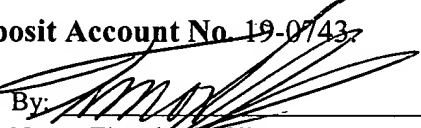
Certificate
FEB 16 2006
of Correction

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

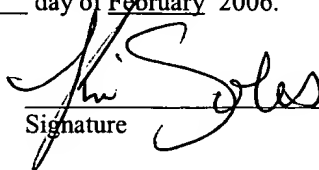
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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer No: 21186

By: 
Name: Timothy B. Clise
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria VA 22313-1450, on this 9 day of February 2006.

Kris Solis
Name


Signature

FEB 21 2006

Patent 6,963,103

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,963,103

Docket No: 1303.028US1

Issue Date: November 8, 2005

Patentee: Leonard Forbes

Customer No.: 21186

Confirmation No.: 1837

Title SRAM CELLS WITH DEPRESSES FLOATING GATE MEMORY, LOW TUNNEL
BARRIER INTERPOLY INSULATORS

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: CERTIFICATE OF CORRECTION BRANCH

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

LEONARD FORBES

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6900

Date :

9 Feb '06

By:

Timothy B. Clise
Reg. No: 40,957
TBC:kms

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Name

Kris Solis

Signature

Kris Solis

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,963,103

Page (2) of 2

DATED : November 8, 2005

INVENTOR(S) : Forbes

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 27, line 5, in Claim 24, after "substrate" insert - - ; - -.

In column 27, lines 6-7, in Claim 24, delete "separate" and insert - - separated - -, therefor.

In column 27, line 22, in Claim 25, delete "elected" and insert - - selected - -, therefor.

In column 27, line 60, in Claim 29, delete "transistor" and insert - - transistors - -, therefor.

In column 28, lines 6-7, in Claim 29, delete "transistor" and insert - - transistors; - -, therefor.

In column 28, line 23, in Claim 31, delete "transistor" and insert - - transistors - -, therefor.

In column 28, lines 26-27, in Claim 31, delete "separate" and insert - - separated - -, therefor.

In column 28, lines 36-37, in Claim 31, delete "transistor" and insert - - transistors - -, therefor.

In column 28, line 41, in Claim 31, after "includes forming" insert - - a - -.

In column 28, line 48, in Claim 32, after "SRAM" insert - - cell - -.

In column 28, lines 53-54, in Claim 32, delete "separate" and insert - - separated - -, therefor.

In column 30, lines 11-12, in Claim 38, delete "separate" and insert - - separated - -, therefor.

MAILING ADDRESS OF SENDER:

SCHWEGMAN, LUNDBERG, WOESSNER, & KLUTH, P.A.
P.O. BOX 2938
Minneapolis, MN 55402

Atty Docket No: 1303.028US1

PATENT NO. 6,963,103

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Page (1) of 2

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On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 11, below "5,981,350 A 11/1999 Geusic et al. 438/386" insert - - 5,986,932 A * 11/1999 Ratnakumar et al. 365/185.07 - -.

On page 2, in field (56), under "Other Publications", in column 1, line 22, delete "Grimbolt," and insert - - Grimblot, - -, therefor.

In column 24, lines 45-46, in Claim 16, delete "separate" and insert - - separated - -, therefor.

In column 24, line 49, in Claim 16, delete "intergrate" and insert - - intergate - -, therefor.

In column 25, lines 3-4, in Claim 18, delete "separate" and insert - - separated - -, therefor.

In column 25, lines 24-25, in Claim 19, delete "separate" and insert - - separated - -, therefor.

In column 25, line 47, in Claim 20, delete "transistor" and insert - - transistors - -, therefor.

In column 25, line 48, in Claim 20, after "and" insert - - a - -.

In column 25, line 56, in Claim 20, delete "intergrate" and insert - - intergate - -, therefor.

In column 26, line 13, in Claim 22, delete "transistor" and insert - - transistors - -, therefor.

In column 26, line 19, in Claim 22, delete "rate" and insert - - gate - -, therefor.

In column 26, line 43, in Claim 23, delete "transistor" and insert - - transistors - -, therefor.

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